

INDIAN INSTITUTE OF INFORMATION TECHNOLOGY KOTTAYAM

Valavoor P O, Kottayam - 686635, Kerala

An Institution of National Importance

**INTELLIGENT INTEGRATED CIRCUITS & SYSTEMS
(I²CS) RESEARCH LAB**



SUMMER INTERNSHIP 2026

APPLICATION DEADLINE

05.04.2026

ONLINE INTERNSHIP START DATE

05.05.2026

I²CS RESEARCH LAB

I²CS Research Lab at IIIT Kottayam stands as a state-of-the-art lab for cutting-edge research in the semiconductor domain. With strong expertise spanning VLSI design, Embedded systems, and system-level innovation, the lab fosters a culture of deep technical rigor. Driven by motivated students, dedicated researchers, and industry collaboration, I²CS continues to push the frontiers of translational semiconductor research. Research lab is equipped with state-of-the-art EDA tools for IC design under **Chips to Startup (C2S) programme of MeitY, Govt of India**. Facilities in research lab include EDA tools from Synopsys, Cadence, Siemens, and AMD Xilinx FPGA boards, MicroChip SoC boards, Nvidia Jetson, Robotics kits, Renesas Kits, Raspberry Pi, Arduino boards and sensors, Matlab etc.

RESEARCH AREAS

- VLSI system design & FPGA
- Edge computing
- Hardware for AI/ML
- Deep learning architectures
- Quantum Circuits
- Hardware security
- Vibration energy harvesting
- Acoustic source localization
- Analog IC design
- RF Circuits
- Signal Processing
- System On Chip Design



Go to I²CS [WEBSITE](#), Check faculty list, Get consent from faculty through email, Pay fees, Submit application form

DETAILS

Mode of internship: Online

Duration: 4-8 Weeks

Eligibility: UG/PG (Ongoing)

Internship fee: Rs.5000/-

Payment Details: Fee payment through SBI Collect

- Link:

<https://www.onlinesbi.sbi/sbi/collect/icollecthome.htm>

- Select: State -> Kerala, Type of Institution->Educational Institution, Institution Name -> IIIT Kottayam, Payment Category -> i2cs Research Lab

CONTACT

☎ **0482 2202167 | 0482 2202218**

🌐 <http://i2cs.iiitkottayam.ac.in>

✉ i2cs@iiitkottayam.ac.in

HIGHLIGHTS:

- Opportunity for conference publications
- Cutting edge projects in semiconductor domain
- Exposure to industry standard VLSI tools
- Career guidance

MAJOR ACHIEVEMENTS OF UG STUDENTS IN 2025-26:

- **1st Prize, VLSI Design Contest-Microchip, at 38th VLSID Bangalore, 2025**
- **1st Prize, VLSI Design Contest-Microchip, at 39th VLSID, Pune, 2026**
- **1st Prize, DVCON Design Contest, Bangalore, 2025**
- **Best Paper Award, 29th VDAT 2025, Chandigarh**

REGISTRATION

<https://tinyurl.com/bdf2c8w6>

